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·	Application No.	Applicant(s)	
Notice of Allowability	10/696,225	BRUCE, ROBERT	
	Examiner	Art Unit	
	Phuong Phu	2611	
The MAILING DATE of this communication appeal All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIOF the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in or other appropriate commu IGHTS. This application is s	this application. If not includ	ed
1. X This communication is responsive to the Amendment filed	on 5/21/07.		
2. ⊠ The allowed claim(s) is/are <u>1-16</u> .			
<ul> <li>Acknowledgment is made of a claim for foreign priority ur</li> <li>a)  All b) Some* c) None of the:</li> <li>1. Certified copies of the priority documents have</li> </ul>	e been received.		
2. Certified copies of the priority documents have		· · · · · · · · · · · · · · · · · · ·	
<ol> <li>Copies of the certified copies of the priority doc International Bureau (PCT Rule 17.2(a)).</li> <li>* Certified copies not received:</li> </ol>	cuments have been received	d in this national stage applica	ation from the
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	of this communication to file IENT of this application.	a reply complying with the re	quirements
4. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give	itted. Note the attached EXA es reason(s) why the oath or	MINER'S AMENDMENT or N declaration is deficient.	IOTICE OF
5. CORRECTED DRAWINGS ( as "replacement sheets") mus	st be submitted.		
(a)  including changes required by the Notice of Draftspers		( PTO-948) attached	
1) 🗌 hereto or 2) 🔲 to Paper No./Mail Date			
<ul><li>(b) ☐ including changes required by the attached Examiner's Paper No./Mail Date</li></ul>	s Amendment / Comment or	in the Office action of	
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in the	.84(c)) should be written on th he header according to 37 CF	e drawings in the front (not the R 1.121(d).	back) of
<ol> <li>DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT I</li> </ol>	SIT OF BIOLOGICAL MATE FOR THE DEPOSIT OF BIO	ERIAL must be submitted. I PLOGICAL MATERIAL.	Note the
Attachment(s)			
<ol> <li>Notice of References Cited (PTO-892)</li> <li>Dotice of Draftperson's Patent Drawing Review (PTO-948)</li> </ol>	_	formal Patent Application	
2. Motice of Dranperson's Patent Drawing Review (P10-948)		ımmary (PTO-413), Mail Date	
<ol> <li>Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date</li> </ol>		Amendment/Comment	
Examiner's Comment Regarding Requirement for Deposit of Biological Material		Statement of Reasons for Allo	owance
	9.		
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## **DETAILED ACTION**

1. This Office Action is responsive to the Amendment filed on 5/21/07. Accordingly, claims 1-16 are currently pending.

## REASONS FOR ALLOWANCE

- 2. Claims 1-16 are allowed.
- 3. The following is an examiner's statement of reasons for allowance:
- -Regarding to independent claim 1, none of prior art of record teaches or suggests a signal edge detector circuit having an output terminal and an input, said signal edge detector circuit comprising: an input transmission line connected to said input and output terminals to provide a path for a signal transition from said input terminal to said output terminal, said input transmission line including a node between said input and output terminals; a first reflection transmission line having first end, said first reflection transmission line connected at a first end to said node providing a reflection of said signal transition, said reflection of a polarity to terminate said signal transition at said output terminal; a second reflection transmission line having a first end, said second reflection transmission line connected at said first end to said node providing a reflection of said signal transition, said reflection of a polarity to cancel out said reflection from said first reflection transmission line at said node.

-Regarding to independent claim 9, none of prior art of record teaches or suggests a clock signal generator comprising a signal edge detector circuit, said signal edge detector circuit comprising an input transmission line connected to said input and output terminals to provide a path for each signal transition in said data stream from said input terminal to said output terminal, said input transmission line including a node between said input and output terminals; a

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first reflection transmission line having a first end, said first reflection transmission line connected at said first end to said node providing a reflection of said signal transition, said reflection of a polarity to terminate said signal transition at said output terminal; and a second reflection transmission line having a first end, said at least one second reflection transmission line connected at said first end to said node providing a reflection of said signal transition, said reflection of a polarity to cancel out said reflection from said first reflection transmission line at said node.

-Regarding to independent claim 16, none of prior art of record teaches or suggests a signal edge detector circuit having an output terminal and an input terminal, said signal edge detector circuit comprising: an input transmission line connected to said input and output terminals to provide a path for a signal pulse from said input terminal to said output terminal, said input transmission line including a node between said input and output terminals; means connected to said node for providing a first reflection of said pulse, said reflection of a polarity to terminate said pulse at said output terminal; means connected to said node for providing a second reflection of said pulse, said reflection of a polarity to cancel out said reflection from said first reflection transmission line at said node.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Phuong Phu whose telephone number is 571-272-3009. The examiner can normally be reached on M-F (8:00 AM - 4:30 PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jay Patel can be reached on 571-272-2988. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Phuong Phu Primary Examiner Art Unit 2611

Phuong Phu 05/24/07 THUONG PHU RY EXAMINER